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Synthesis of ZnO and CuO Nanowires by Thermal Oxidation on Metallic Substrates

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Abstract. In this research work, brass (Cu - 37.2 wt% Zn) and Cu (99.9 wt%) wires having diameters of 200 μm were thermally oxidized in N_2 containing 5% O_2 , at a flow rate of 200 sccm and in the ambient atmosphere respectively, to support the growth of nanowires. The oxidation temperature was varied from 300 to 600 $^\circ\text{C}$ and the as-grown nanowires were characterized by field emission scanning electron microscope (FESEM) equipped with energy dispersive X-ray (EDX) spectroscope, and transmission electron microscope (TEM). Results show that ZnO and CuO nanowires are formed on brass and Cu wires, respectively. The ZnO nanowires are branched and CuO nanowires are straight with tapered morphology. ZnO nanowires having hexagonal wurtzite structure grow along the $\langle 11\bar{2}0 \rangle$ directions whereas, CuO nanowires have monoclinic structure. A diffusion based stress induced model is proposed to explain the growth mechanism of the nanowires. Thermal oxidation process is a suitable platform for synthesizing ZnO and CuO nanowires, which can be used in *in-situ* device fabrication.

Introduction

There is an ongoing interest in zinc oxide (ZnO) and cupric oxide (CuO) based semiconducting metal oxide nanostructures due to their unique electrical, chemical, magnetic, and optical properties. For example, ZnO is a *n*-type material that has a direct wide band gap of 3.37 eV at 300 K and large exciton binding energy of 60 meV [1-3]. Some of the opto-electronic and nano-electronic properties of ZnO overlap with GaN, which is another wide bandgap semiconductor ($E_g \sim 3.4$ eV at 300 K) possessing of attractive electrical, optical, and thermal properties [2, 4]. However, ZnO has some advantages over GaN, such as it has simpler routes for crystal growth and easy availability, which results in a lower cost of ZnO based devices [2]. On the other hand, CuO is a *p*-type material with narrow bandgap energy of 1.2 eV and conductivity of 10^{-7} - 10^{-3} $\text{S}\cdot\text{cm}^{-1}$ [5, 6]. CuO is an anti-ferromagnetic material with a Néel temperature of 230 K, which exhibits super-thermal conductivity, photovoltaic properties, high stability, and antimicrobial activity [7]. These unique combinations of properties in ZnO and CuO based materials draw attention to the researchers and they have been extensively studied in light of their many potential engineering applications, such as UV laser [1], photo-detector [8, 9], field emitter [10, 11], gas sensor [12, 13], solder cell [14, 15], Li-ion battery [16, 17], supercapacitor [18, 19], and catalyst [20, 21]. For this reason, there is an ongoing effort to synthesize long, low-defect and highly crystalline ZnO and CuO nanowires at a lower cost.

In the recent years, a rich family of morphological variations of ZnO and CuO based nanostructures has been synthesized by various innovative techniques, such as hydrothermal [12,

22], electrospinning [23, 24], ultrasonic irradiation [25, 26], sol-gel [27, 28], chemical vapour deposition [20, 29], thermal evaporation [30, 31], pulsed laser deposition [32, 33], RF sputtering [34, 35], and molecular beam epitaxy [36, 37]. However, these methods often have associated problems including removal of catalyst and template, multi-steps and complex synthesis procedure, poor adhesion with underlying substrate, less scalability and expensive equipment [38]. Alternatively, thermal oxidation is a simple, single-step and inexpensive route for the formation of metal oxide nanostructures directly on metal substrates [38-42], which is desirable for many *in-situ* device fabrication processes [40]. In principle, nanowires of metal oxide based arrays synthesized by thermal oxidation can be electrically addressed by the supporting interdigitated electrodes for some applications, such as gas sensor and field emitter [40, 43, 44]. The resultant nanostructures obtained by thermal oxidation are highly crystalline and pure, which do not require any further treatment [38-42].

Due to engineering importance of ZnO and CuO nanostructures, recently combined effect of both ZnO and CuO nanostructures has been studied for various applications, including catalyst [45, 46], solar cell [47, 48], gas sensor [49], field emitter [50], and optoelectronics [51]. Promising results were obtained for the combined use of ZnO and CuO in the above mentioned applications [45-51]. Development of a simple, single step, scalable and catalyst-free platform for co-synthesising ZnO and CuO based nanostructures on metal substrates for the above mentioned applications will significantly reduce experimental complexity, time, and cost. Thermal oxidation can be an advantageous solution in these aspects.

In this research, thermal oxidation process is utilized for obtaining semiconducting one dimensional (1D) of ZnO and CuO nanowires on brass (Cu - 37.2 wt% Zn) and Cu (99.9%) wires, respectively. The oxidation temperature is optimized for the growth of nanowires. The nanowires and oxide layer beneath the nanowires are characterized by FESEM, EDX spectroscope, and TEM. Finally, a diffusion based stress induced mechanism is proposed for the growth of ZnO and CuO nanowires on brass and Cu substrates, respectively.

Experimental Procedure

As-received brass (Cu - 37.2 wt% Zn) and Cu (99.9%) wire having diameter of 200 μm was used in thermal oxidation. The experimental setup for thermal oxidation is described in the previous work [38]. In short, 5 cm long brass or Cu wire was placed in an alumina boat inside a horizontal tube furnace. The furnace was heated from ambient to 300-600 $^{\circ}\text{C}$ at a heating rate of 30 $^{\circ}\text{C}/\text{min}$. The brass wire was oxidized in the presence of 5% O_2 in N_2 flown at a rate of 200 sccm, whereas the Cu wire was oxidized in ambient atmosphere. At the peak temperature, the wires were oxidized for 4 h. After 4 h of oxidation at the peak temperature, the furnace was switched off and cooled to room temperature by opening the lid of the furnace. The oxidation conditions of brass and Cu wire is presented in Table 1.

Table 1: Experimental conditions for the growth of nanowires on brass and Cu wires, respectively.

<i>Substrate</i>	<i>Oxidation temperature</i>	<i>Oxidation environment</i>	<i>Flow rate</i>	<i>Oxidation duration</i>
Brass (Cu - 37.2 wt% Zn) wire	300-600 $^{\circ}\text{C}$	5% O_2 in N_2	200 sccm	4 h
Cu wire	300-600 $^{\circ}\text{C}$	Ambient	-	4 h

The oxidized brass and Cu wires were characterized by FESEM equipped with EDX spectroscope. InLens detector was used in FESEM for the observation of oxidized surface. EDX spot analysis, line scan and elemental mapping were performed on the nanowires and the oxide scales beneath the nanowires. The nanowires were also observed under a high resolution transmission electron microscope (HRTEM: FEI Tecnai F-20). For this, oxidized brass wire was taken in a small bottle followed by the addition of 2.5 ml deionized water. Then the bottle was

ultrasonicated for 30 sec to scale off the nanowires. A tiny amount of the suspension was dropped on Cu grid using a micro-pipette followed by drying in oven at 60 °C for 1 h. Then the Cu grid was placed inside the HRTEM chamber for the high resolution observation of the oxides. The selected area diffraction (SAD) pattern was obtained from fast Fourier transformation (FFT).

Results and Discussion

Brass (Cu - 37.2 wt% Zn) wires were oxidized in the presence of 5% O₂ in N₂ at a flow rate of 200 sccm at temperatures ranging from 300 to 600 °C for 4 h. After oxidation, the brass wires became greyish in colour.

The FESEM images of brass wires after 4 h of oxidation at different temperatures are shown in Fig. 1. For oxidation temperatures of 300 °C, short and inconsistent forms of one dimensional (1D) nanostructures are seen (Fig. 1(a)). With increasing oxidation temperature to 350 °C, the length (1-2 μm) and coverage of the nanowires are increased (Fig. 1(b)) [38]. At oxidation temperature of 400 °C, combination of long and short nanowires is seen (Fig. 1(c)) [38]. The length of long nanowires is up to 6 μm from the root to the tip. For the oxidation temperature of 450 °C, high coverage of long nanowires with thin and thick morphologies is seen all over the brass wire (Fig. 1(d)) [38]. The diameter of thin nanowires is 50-200 nm with length of up to 30 μm. On the other hand, the diameter and length of thick nanowires is 200-500 nm and 5-15 μm, respectively with faceted morphology. Further increase in temperature to 500 °C, the nanostructures become shorter and the oxide scale exfoliates from the core of brass wire (Fig. 1(e)) [38]. This could be due to the reason that the $\alpha\text{-}\beta'$ phases in brass (Cu - 37.2 wt% Zn) transforms to $\alpha\text{-}\beta$ at 458 °C [52]. The transformation of β' to β may attributes to the growth of shorter nanowires at 500 °C [38]. The huge difference in a -axis thermal expansion coefficient of brass ($19 \times 10^{-6} \text{ K}^{-1}$) and ZnO ($2.9 \times 10^{-6} \text{ K}^{-1}$) is the reason for the exfoliation of oxide layers during cooling from 500 °C [38]. Beside this, the nanostructures transform to flat-cone shaped morphology at 500 °C [38]. Increasing temperature up to 600 °C, the length and coverage of the nanowires start increasing (Fig. 1(f) and 1(g)) [38]. So, from the results it is clear that 450 °C is the optimum temperature for the growth of nanowires on brass wire during thermal oxidation.

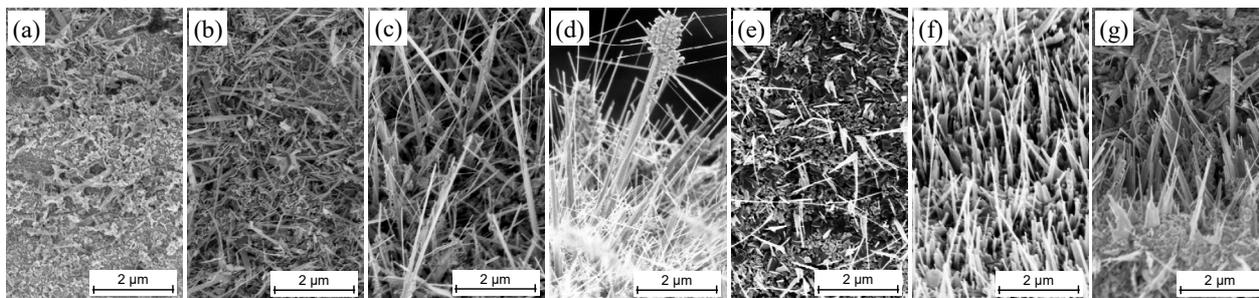


Fig. 1: FESEM images of brass (Cu - 37.2 wt% Zn) wires oxidized in the presence of 5% O₂ in N₂ flown at a rate of 200 sccm at temperatures of (a) 300 °C, (b) 350 °C, (c) 400 °C, (d) 450 °C, (e) 500 °C, (f) 550 °C, and (g) 600 °C.

Branching of nanowires is seen during oxidation of brass wire, prominently at 450 °C as seen in Fig. 2(a). The branches are originated from the irregularly shaped particles deposited on the top and side walls of thick nanowires. The length of the branches is 1-3 μm. In all cases, the branches are aligned at 60° angle with the main stem of the nanowires. Though some branches in Fig. 2(a) appear to be perpendicular to the main stem, basically they are originated at 60° angle with the main stem. The angles of branches would be obvious when they are observed from correct perspectives. Some sort of entanglement is also observed in branches, which could be due to the presence of defect sites. The elemental mapping of the nanowires is shown in Fig. 2(b-d) and the presence of Zn and O is obvious (Fig. 2(b, c)), which confirms that the nanowires are ZnO. The presence of Cu is also observed in the elemental mapping (Fig. 2(d)). It is expected that some Cu in the form of CuO is present in nanowires. However, further studies are required to confirm the CuO doping in ZnO nanowires.

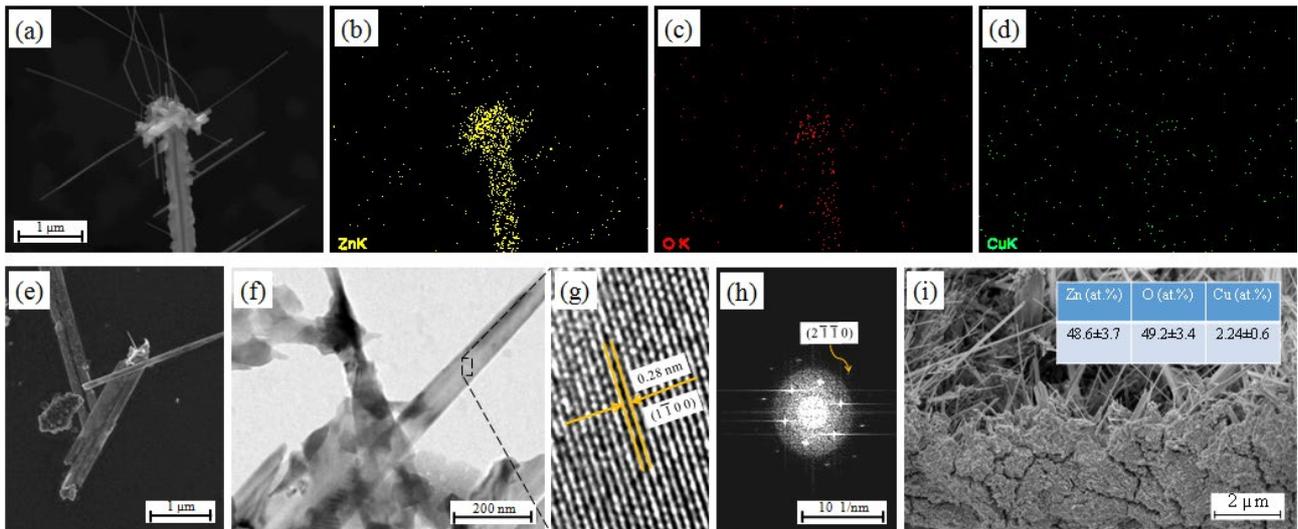


Fig. 2: (a) FESEM image of branched ZnO nanowires grown at 450 °C, (b-d) corresponding EDX spectroscopy elemental mapping of Zn, O and Cu, respectively, (e) FESEM images of thick nanowires on TEM grid, (f) bright-field TEM image of thin nanowires, (g) corresponding HRTEM image, (h) corresponding SAD pattern, and (i) exfoliated oxidized layer in cross-sectional view and corresponding EDX spot analysis of the oxidized layer (inset).

The ZnO nanowires were observed under a TEM. To prepare the samples, the ZnO nanowires were taken on Cu TEM grid and observed under FESEM as seen in Fig. 2(e) followed by observation under HRTEM. It appears from Fig. 2(e) that the thick ZnO nanowires consist of a few thin nanowires. Probably during oxidation at higher temperatures they fused together due to the diffusion of atoms. Fig. 2(f) shows the bright-field TEM image of ZnO nanowires having a thickness of 50 nm. The HRTEM image of the main stem of the nanowires shows the (1 $\bar{1}$ 0) crystal planes of hexagonal wurtzite ZnO (Fig. 2(g)). The SAD pattern focusing on the main stem confirms that the ZnO nanowires grow along the $[2 \bar{1} \bar{1} 0]$ directions (Fig. 2(h)). In our previous study, we have confirmed that the branches of the ZnO nanowires grow along the $[1 1 \bar{2} 0]$ direction [38]. It should be noted that $[2 \bar{1} \bar{1} 0]$ and $[1 1 \bar{2} 0]$ is the same family of direction and are angled at 60° with each other. So, it is clear that the ZnO nanowires and its branches grow along the $\langle 1 1 \bar{2} 0 \rangle$ directions.

The FESEM image of the exfoliated oxidized layer on brass wire is shown in Fig. 2(i) in cross-section. The EDX spectroscopy spot analysis is performed on the oxide layer beneath the ZnO nanowires. The EDX spectroscopy analysis confirms that the oxide layer is ZnO with some Cu content (~2 at%). It is expected that during oxidation, Cu also migrates to the surface together with Zn and reacts with oxygen to form CuO.

The growth mechanism ZnO nanowires on brass (Cu - 37.2 wt% Zn) substrate was proposed in our previous work [38]. As the liquidous temperature of brass alloy containing 37.2 wt% Zn is significantly higher (~900 °C) compared to the oxidation temperature, it is expected that no melting is involved in the present case [38]. So, vapour-liquid-solid (VLS) or vapour-solid (VS) mechanism is not applicable for the growth of ZnO nanowires on brass substrate as explained by others for the growth of ZnO nanowires on pure Zn substrates [53-55]. According to the proposed mechanism, the Zn atoms migrate towards the surface of brass wire to form ZnO layer and as a result the surface becomes Zn rich [38]. The crystallographic structure of ZnO is hexagonal wurtzite and brass (Cu-37.2 wt% Zn) is a solid solution of face centred cubic and body centred cubic structures. It is expected that a compressive stress is generated at the oxide-metal interface due to the mismatches in volume, density and crystallographic structures of ZnO layer and brass substrate [38]. This compressive stress is released by forming new surfaces in the form of ZnO nanowires on the ZnO layer during oxidation [56]. The side-walls of ZnO nanowires are facing to (0 0 0 1) planes, which is favourable for transporting of Zn atoms from the root to the tip of the nanowires [44]. Zn atoms at

the tip of the nanowires react with oxygen for further growth of the nanowires. It should be noted that the vapour pressure of Zn (0.491 mbar) is significantly higher than Cu ($3.12 \times 10^{-15} \text{ mbar}$) at $450 \text{ }^\circ\text{C}$ [57]. So, it is expected that some fraction of diffused Zn vaporized and reacts with oxygen to form ZnO, which deposits on the family of $\langle 11\bar{2}0 \rangle$ crystal planes of the growing ZnO nanowires (Fig. 2(a)). Stress is accumulated at the nucleation sites and relaxed by forming branches of ZnO nanowires. The $\langle 11\bar{2}0 \rangle$ crystal planes are angled at 60° with each other and for this reason the branches are aligned at 60° with the main stem of the ZnO nanowires. Little amount of Cu is also diffused to the surface during oxidation and it is expected that the ZnO nanowires are doped with CuO. However, further studies are required to confirm this fact.

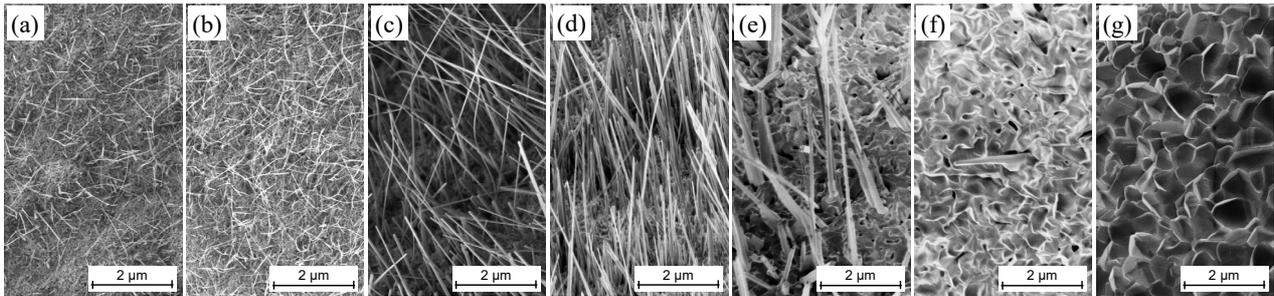


Fig. 3: FESEM images of Cu wires oxidized in ambient at temperatures of (a) $300 \text{ }^\circ\text{C}$, (b) $350 \text{ }^\circ\text{C}$, (c) $400 \text{ }^\circ\text{C}$, (d) $450 \text{ }^\circ\text{C}$, (e) $500 \text{ }^\circ\text{C}$, (f) $550 \text{ }^\circ\text{C}$, and (g) $600 \text{ }^\circ\text{C}$.

The Cu wires were also oxidized at $300\text{--}600 \text{ }^\circ\text{C}$ in the ambient. After oxidation Cu wires turned to black colour. Fig. 3 shows the oxidized surface of Cu wires at different oxidation temperatures. Low coverages of nanowires are seen at the oxidation temperature of $300 \text{ }^\circ\text{C}$ (Fig. 3(a)). With increasing the temperatures until $450 \text{ }^\circ\text{C}$, the diameter, length, and coverage of the nanowires are increased as seen in Fig. 3(b-d). At temperature of $450 \text{ }^\circ\text{C}$, nanowires having length up to $10 \text{ }\mu\text{m}$ is seen with diameter of $80\text{--}200 \text{ nm}$. At $500 \text{ }^\circ\text{C}$, the coverage of nanowires is decreased (Fig. 3(e)). Beyond $500 \text{ }^\circ\text{C}$, oxide scale is observed (Fig. 3(f) and Fig. 3(g)). From the results, $450 \text{ }^\circ\text{C}$ is considered the optimum temperature for the growth of nanowires on Cu wire.

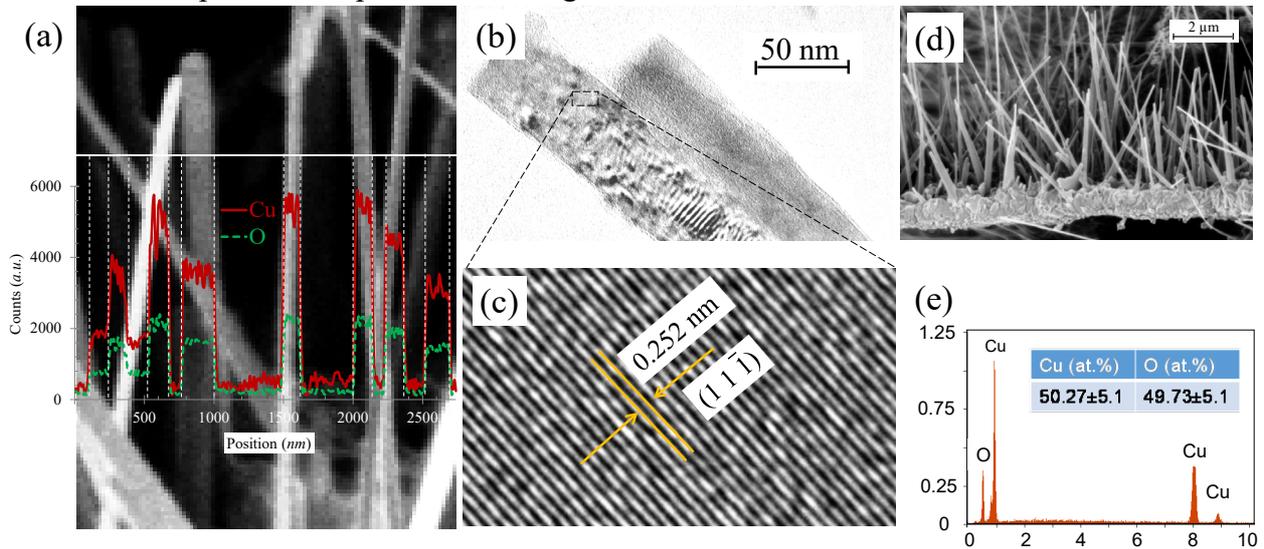


Fig. 4: (a) FESEM image of CuO nanowires grown at $450 \text{ }^\circ\text{C}$ and corresponding elemental line scanning, (b, c) bright-field TEM image of nanowires and corresponding HRTEM image, (d, e) exfoliated oxidized layer in cross-sectional view and corresponding EDX spot analysis.

Fig. 4(a) shows the FESEM images of few nanowires and corresponding line scanning. The nanowires are straight with no branching. Beside this, the nanowires are slightly tapered from the bottom to the tip. The presence of Cu and O is confirmed from the line scanning and thus it is concluded that the nanowires are CuO. Fig. 4(b) shows the bright-field TEM image of CuO

nanowires and the corresponding HRTEM image is shown in Fig. 4(c). The HRTEM image shows the $(1\ 1\ \bar{1})$ crystal planes of monoclinic CuO (Fig. 4(c)). The FESEM image of exfoliated oxidized layer of Cu wire is shown in Fig. 4(d) and the corresponding EDX spectroscopy spot analysis in Fig. 4(e). It is confirmed from the atomic ratios in EDX spectroscopy analysis that oxide layer beneath the CuO nanowires is also CuO. However, in the literature formation of Cu₂O is observed beneath the CuO layer [58, 59], which was not peeled off during exfoliation in the present case. So, the configuration of Cu wire after oxidation from outermost interface to innermost interface is as follows: CuO nanowires - CuO layer - Cu₂O layer - Cu core.

Previously, stress induced mechanism was proposed by Kumar *et al.* [56] to explain the growth of CuO nanowires on Cu substrate by thermal oxidation. During oxidation, CuO nanowires grow directly on intermediate CuO layer, which grows on inner Cu₂O layer [58, 59]. At the initial stage of oxidation, Cu atoms react with the oxygen and CuO layer is formed at the surface. At the later stage, Cu²⁺ ions diffuse to the surface through the grain boundaries and O²⁻ counter diffuse [59-62] and as a result Cu₂O layer is formed beneath the CuO layer. The formation of simple cubic Cu₂O underneath monoclinic CuO layer during oxidation induces a substantial stress at the interface, which is relaxed by forming CuO nanowires [56]. Diffusion of oxygen from outer surface to the interface of CuO/Cu₂O layers also accumulates stress, which favors the growth of nanowires [56]. The grain boundary runs along the entire length of the nanowires and acts as a path for short-circuit diffusion transport of Cu ions to the tip for the growth of nanowires [60].

Table 2: Summary of thermal oxidation process and characterizations of brass and Cu wires.

Criterion	Parameter	Brass (Cu - 37.2 wt% Zn) wire	Cu wire
Process parameter	Optimum oxidation temperature	450 °C	450 °C
	Oxidation environment	5% O ₂ in N ₂ flown at a rate of 200 sccm	Ambient
Characterization	Composition	ZnO. The nanowires may be doped with CuO. Further studies are required.	CuO
	Morphology	Thin and thick nanowires with branches. At oxidation temperatures of ≥ 500 °C, flat-cone shaped morphology is seen.	Straight nanowires with slightly tapered morphology.
	Diameter and length (at oxidation temperature of 450 °C)	Diameter: 50-500 nm Length: up to 30 μ m	Diameter: 80-200 nm Length: up to 10 μ m
	Crystal structure	Wurtzite	Monoclinic
	Growth direction	$\langle 1\ 1\ \bar{2}\ 0 \rangle$	-
	Oxide layer beneath the nanowires	ZnO	CuO and Cu ₂ O
Growth mechanism	-	Diffusion based stress induced mechanism	Diffusion based stress induced mechanism

Table 2 summarizes the growth of ZnO and CuO nanowires on brass and Cu wires during thermal oxidation, respectively. Both of the nanowires optimally grow at 450 °C in a single experimental setup. As both ZnO and CuO nanowires have engineering importance and in many instances, they are used combinedly for various applications, including catalyst [45, 46], solar cell [47, 48], gas sensors [49], field emitter [50], and optoelectronics [51], the thermal oxidation process could be a lucrative approach for the growth of 1D ZnO and CuO nanowires. Beside, this process provides an easy, inexpensive, and highly scalable platform for the growth of these nanowires. In addition, this method also facilitates the growth of nanowires directly on the substrate, making it particularly suitable for many *in-situ* device fabrication processes, including gas sensors and field emitters [40, 43, 44].

Conclusions

In this work, brass (Cu - 37.2 wt% Zn) and Cu wires were oxidized for the growth of nanowires. The nanowires and the oxide scale beneath the nanowires were characterized. The key findings of the research can be summarized as shown below:

- ZnO are formed on brass wire during thermal oxidation in the presence of 5% O₂ in N₂ flown at a rate of 200 sccm at 450 °C. The nanowires may be doped with CuO. Further studies are required to confirm this fact. On the other hand, CuO nanowires are formed on Cu wires during thermal oxidation in ambient at 450 °C.
- ZnO nanowires have thin and thick morphologies with branches. The diameter of the ZnO nanowires is 50-500 nm and lengths up to 30 μm. The branches are 1-3 μm long. On the other hand, CuO nanowires are straight with slightly tapered morphology. The diameter of the CuO nanowires is 80-200 nm and lengths up to 10 μm.
- ZnO nanowires having hexagonal wurtzite structure grow along the $\langle 11\bar{2}0 \rangle$ directions. The CuO nanowires have monoclinic structure.
- ZnO layer is formed beneath the ZnO nanowires during the oxidation of brass substrates. On the other hand, CuO and Cu₂O layers are formed beneath the CuO nanowires during the oxidation of Cu substrates.
- A diffusion-based stress induced model is proposed to explain the mechanism of ZnO and CuO nanowires growth on brass and Cu substrates, respectively.

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